



**A D PATEL INSTITUTE OF TECHNOLOGY, NEW V. V. NAGAR
DEPARTMENT OF ELECTRICAL ENGINEERING**

Date: 6th July 2017

Research position

Applications are invited for the ONE temporary post of **Junior Research Fellow (JRF)** in the DST sponsored project entitled “**DESIGN AND DEVELOPMENT OF DIGITAL PROTECTION FOR POWER TRANSFORMER**”. The position is completely temporary for the duration of three year and co-terminus with the project. The institute may select candidate against the post depending upon qualification and experience of candidates and reserves the right to relax any of the qualifications.

Qualifications: 1st class in M.E/M/Tech in Electrical Engineering and related fields. Preference will be given to applicants who are NET/ GATE qualified and/or well versed in Power System.

Salary: Fixed remuneration (As per the norms of the institute and funding agency.)

Instruction for submitting applications:

- The application (only hard copy) as per attached format with photocopies of the certificates, mark sheets and testimonials should reach to the Principal Investigator.
- Application received after last date shall not be entertained. Institute shall not be responsible for any postal delay.
- No email copy of the application will be entertained.

Closing date of Application: 5th August, 2017. Only Shortlisted candidates will be called for interview by email and/or phone. Please note that **No TA/DA** will be given to attend the interview. Applications (*by post/courier*) are to be sent to the below mentioned address.

Principal Investigator

Dr. Nilesh Chothani
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